



Substitute for form 1449B/PTO			Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application Number	09/802,017
			Filing Date	March 8, 2001
			First Named Inventor	Saulsbury, Ashley
			Art Unit	
			Examiner Name	
Sheet	3	of	Attorney Docket Number	016747-009910US

U.S. PATENT DOCUMENTS+					
Examiner Initials*	Cite No. ¹	Document Number Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
GJP	AA	US-5,953,738	9/1999	Rao	RECEIVED NOV 12 2003 Technology Center 2100
GJP	AB	US-6,202,143	3/2001	Rim	
GJP	AC	US-6,256,256	7/2001	Rao	
GJP	AD	US-6,321,318	11/2001	Baltz et al.	
	AE				

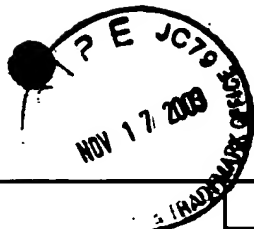
FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
	AF							<input type="checkbox"/>
	AG							<input type="checkbox"/>

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AH		
	AI		

Examiner Signature	GARY J. PORTKA	Date Considered	5/11/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.



PTO/SB/08B (08-03)

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			Examiner Name	Portka, Gary J.
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Sheet	3	of		

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	AB	US-			

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
GJP	AE	NUNOMURA, YASUHIRO, "M32R/D-Integrating Dram and Microprocessor," IEEE Micro, IEEE Inc. New York, US., Vol. 17, No. 6, November 1, 1997, pp. 40-48, XP-000726003; ISSN 0272-1732.	
GJP	AF	E. KOZYRAKIS, "Scalable Processors in the Billion-Transistor Era: IRAM," IRAM Computer, IEEE Computer Society, Long, Beach, CA, Vol. 30, No. 9, September 1, 1997, pp. 75-78, XP-000730003.	
GJP	AG	HERMANN, KLAUS, et al., "Architecture of a Multiprocessor System With Embedded DRAM for a Large Area Integration," IEEE International Conference on Innovative Systems in Silicon, Piscataway, NJ, October 8, 1997, XP002179990.	

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Examiner Signature	GARY J. PORTKA	Date Considered	5/11/04
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